



(11) Publication number: **63248239 A**

# PATENT ABSTRACTS OF JAPAN

**(51) Intl. Cl.: H04J 7/00**

(22) Application date: 03.04.87

(71) Applicant: **RICOH CO LTD**

(72) Inventor: SHIBATA ISAMU

**(74) Representative:**

**(57) Abstract:**

value signal to apply modulation drive to a light source 3 to output an optical signal. Part of the optical output is subjected to photoelectric conversion by a photodetector 5. This signal is fed back to the circuit 2 via a peak hold circuit 7, a comparator 8 and an LPF 9 to the circuit 2 so as to make the maximum value of the output of the light source 3 equal to a reference signal voltage of the comparator 8. A receiver holds the maximum value of the input signal by the peak hold circuit 12, that is, holds the voltage of the line synchronizing signal. Voltage division circuits 13-15 form reference voltages VR1-VR3 from the output signal of the circuit 12, comparators 16-18 use the reference voltage to binarize the output of the amplifier 11 to obtain said 3 signals. A DFF 19 obtains the original picture signal from the clock signal by the comparator 18 through the use of the picture signal from the comparator 17.

COPYRIGHT: (C)  
1988,JPO&Japio